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T-820 P02/09 U-450

Appl. No. 10/692,636

Amdt. dated September 8, 2004

Reply to Office action of June 9, 2004

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended). A semiconductor component, comprising:

a semiconductor chip with a semiconductor memory having an array with a plurality of memory cells;

at least one connection contact;

an electrically conductive connection integrated into said semiconductor chip between said at least one connection contact and said semiconductor memory;

at least one converter device selected from the group consisting of a digital-analog converter and an analog-digital converter integrated into said semiconductor chip and incorporated in said electrically conductive connection for enabling a utilization of said semiconductor memory with analog signals; and

wherein a number of said connection contacts for said semiconductor chip is less than a number of connection

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contacts required for a utilization of said semiconductor memory with digital signals.

Claim 2 (original). The component according to claim 1, wherein said at least one converter device includes digital-analog converters and analog-digital converters disposed to enable read and write operations by outputting and inputting analog data.

Claim 3 (original). The component according to claim 1, wherein said at least one converter device includes at least one analog-digital converter disposed to enable control of read and write operations and/or to address memory locations by inputting analog control signals.

Claim 4 (original). The component according to claim 1, wherein said at least one converter device includes digital-analog converters and analog-digital converters disposed to enable both read and write operations and control thereof by outputting and inputting exclusively analog data and by inputting exclusively analog control signals.